

IN THE UNITED STATES PATENT OFFICE

Applicant: Steven P. Young

Assignee: Xilinx, Inc.

Title: "Columnar Architecture"

Serial No.: Not Yet Known

File Date: October 10, 2003

Examiner: Not Yet Known

Art Unit: Not Yet Known

Continuation In Part of

Serial No.: 10/618,404

File Date: July 11, 2003

Docket No.: X-1392-1P US

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

INFORMATION DISCLOSURE STATEMENT

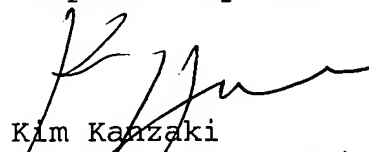
Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicant brings to the attention of the Examiner the Six (6) references listed in the attached Substitute for Form PTO-1449 (Information Disclosure Statement by Applicant).

Four of these references were cited in prior related U.S. patent application Serial Number 10/618,404 filed July 11, 2003 to which this application claims priority. Copies of these references have not been supplied herein since they were previously submitted in the parent case. One additional reference cited is a patent therefore a copy has not been enclosed. The last additional reference cited is a non-patent reference and a copy has been enclosed herewith.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully Submitted,


Kim Kanzaki
Attorney for Applicant
Reg. No. 37,652

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| | | | | Applicant / Confidential | Unknown / |
| | | | | Filing Date | October 10, 2003 |
| | | | | First Named Inventor | Steven P. Young |
| | | | | Art Unit | Unknown |
| | | | | Examiner Name | Unknown |
| | | | | Attorney Docket Number | X-1392-1P US |
| Sheet | 1 | of | 2 | | |

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| | | | |
|-----------------------|--|--------------------|--|
| Examiner Signature | | Date Considered | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Applicant's unique citation designation number (optional). ² See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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| | | | | Filing Dat | October 10, 2003 |
| | | | | First Named Invent r | Steven P. Young |
| | | | | Art Unit | Unknown |
| | | | | Examiner Name | Unknown |
| Sheet | 2 | of | 2 | Attorney Docket Number | X-1392-1P US |

| OTHER – NON PATENT LITERATURE DOCUMENTS | | | |
|---|--------------------------|---|----------------|
| Examiner Initials * | Cite No. ¹ | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published. | T ² |
| | | Xilinx, Inc.; DS083-2 (v2.7) Advance Product Specification; "Virtex-II Pro Platform FPGAs: Functional Description"; available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; pp. 1-48. | |
| | | Xilinx, Inc.; DS031-2 (v1.9) Advance Product Specification; "Virtex-II Platform FPGA Handbook," available from Xilinx, Inc., 2100 Logic Drive, San Jose, California 95124; November 29, 2001, pp. 77-79. | |
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